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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

INVENTOR(S): Kunnari et al.

SERIAL NO.: 10/815,887

ART UNIT: 2631

FILED: March 31, 2004

EXAMINER: Not Yet Assigned

TITLE: Circuit Arrangement And a Method To Transfer Data On a 3-Level Pulse
Amplitude Modulation (PAM-3) Channel

CONFIRMATION NO.: 9083

ATTORNEY DOCKET NO.: 872.0182.U1(US)

MAIL STOP AMENDMENT
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

(37 C.F.R. §1.97(d))

Dear Sir:

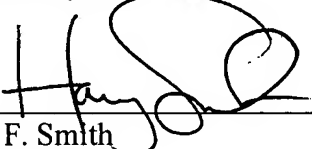
The following information is being disclosed to the U.S. Patent and Trademark Office as information that may be material to the examination of the above-identified patent application.

Applicants' Attorney is aware of the art listed on the enclosed Form PTO-1449. Copies of the non-U.S. patent art are enclosed with the Form PTO-1449 for the Examiner's use.

Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement. Enclosed is a copy of a partial International Search Report listing the cited references.

The filing of this Statement is not to be construed as a representation that a search has been made regarding the claimed invention (37 C.F.R. §1.97(g) or that no other possible material information exists. In addition, the filing of this Statement is not to be construed to be an admission that the information cited in the Statement is, or is considered to be, material to Patentability (37 C.F.R. §1.97(h)).

Respectfully submitted,



Harry F. Smith
Reg. No. 32,493

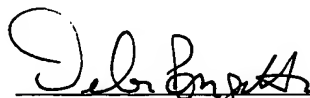
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date shown below in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Serial No.: 10/815,887

Filing Date: March 31, 2004

Group: 2631

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)

	TEIFEL, J. et al., "A High-Speed Clockless Serial Link Transceiver", Publication Date May 12-15, 2003, pgs. 1-11. Asynchronous Circuits and Systems 2003, Proceedings of the Ninth International Symposium.
	SMITH, R. et al., "High-Level Design for Asynchronous Logic", Publication Date 2001, pgs. 431-436, Design Automation Conference 2001, Proceedings of the ASP-DAC 2001.
	ROÏNE, P.T. et al., "A System for Asynchronous High-speed Chip to Chip Communication, Publication Date March 18-21, 1996, pgs. 2-10, Advanced Research in Asynchronous Circuits and Systems 1996.
	YANG, C. K. et al., "Analysis of Timing Recovery for Multi-Gbps PAM Transceivers", Publication Date September 21-24, 2003, pgs. 67-72, Custom Integrated Circuits Conference 2003.

Date Considered:

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.
Include a copy of this citation form with your next correspondence to the Applicant(s).